St Joseph's Degree & PG College

Department of Physics & Electronics

Odd Semester Report

(June-October)

2014-15

Department of Physics & Electronics
Odd Semester Departmental Activity Report-2014

Name of the Activity:	Orientation Programme
Date:	June 2014
Incharge/Coordinator:	Mrs.L.Mihira Priya
Venue:	Seminar Hall-Cellar,St.Joseph's Degree & PG College
Target Group:	I B.Sc (All Groups)
Resource Person & Organization:	1.Principal 2. Director

Objective:

- To introduce the academic structure.
- To acquaint with the college rules & guidelines.

About the Activity (few lines):

First year students often feel ill-equipped academically and emotionally, hindering their integration and adjustment into college life. *A* standardised orientation program for first year undergraduate students organized focusing on peer and academic support based principles, assisting commencing students to achieve a smooth transition from intermediate level into university level under graduation programme.

We introduced activities which are focused on information for the first year, incorporation of Alumni student interaction and the inclusion of ice-breaker activities..

Photos:

About College life-Interaction by Principal

Ice-breaker activities

Alumni interaction

Name of the Activity:	Alumni Meet
Date:	26th July 2014
Incharge/Coordinator:	Mrs L.Mihira Priya
Venue:	Joseph's Hall
Target Group:	Old Batch Students

Objective:

- To maintain an up-to-date and comprehensive database of alumni
- To identify and promote alumni success and achievements to advance the credibility and reputation of the college
- To encourage the Alumni to take an active and abiding interest in the work and progress of the Institute.
- To promote and foster mutually beneficial interaction between the Alumni and the present students.

About the Activity (few lines):

Alumni meet, an initiative taken by us to stay in touch with our alumni and to ensure that our alumni feel connected irrespective of the physical boundaries. These meets have been providing a platform for all alumni to know the progress SJC has made in the years from when they left the college. The meets are not only an occasion for the alumni to get nostalgic and relive their college memories but also they help us to maintain the strong connect with our alumni and gain from their knowledge and expertise of the industry.

Photos:

Chief Guest of the day: Rev.Fr.Amal –B.Sc(MECs) Alumni

Name of the Activity: Entrepreneurship Orientation Programme

Date: 24th July 2014

Incharge/Coordinator: Mrs Savithri ,HoD Department of Commerce.

Venue: Joseph's Hall

Target Group: B.Sc

Resource Person & Organization: Programme conducted by Commerce Department for Science students in collaboration with National Small industries Corporation Ltd (NSIC)

No. of students participated: 10

Objective:

The objective of the workshop was to create awareness amongst the students about the option of setting up new small business enterprises.

About the Activity (few lines):

NSIC Organized three Day Entrepreneurship Orientation Training Programme to encourage the budding entrepreneur, who are still pursuing their studies in Colleges and do not have any idea about the Entrepreneurship or how to start any new business and what problems are faced to start the new business. This programme offers the insight into the self employment opportunities and develops the confidence to become self employed..

Name of the Activity:	Workshop on VHDL
Date:	30th Sep 2014
Nature of Activity:	Staff & student enrichment programme
Incharge/Coordinator:	Mrs. L.Mihira Priya
Venue:	MCA Lab
Target Group:	III B.Sc(Elec)
Resource Person & Organization:	1. P.V Kiran Kumar (Marketing Manager) 2. K.Dasharath (Senior application Engineer) 3. S.Ramesh(Customer support Engineer) Physitech Electronics

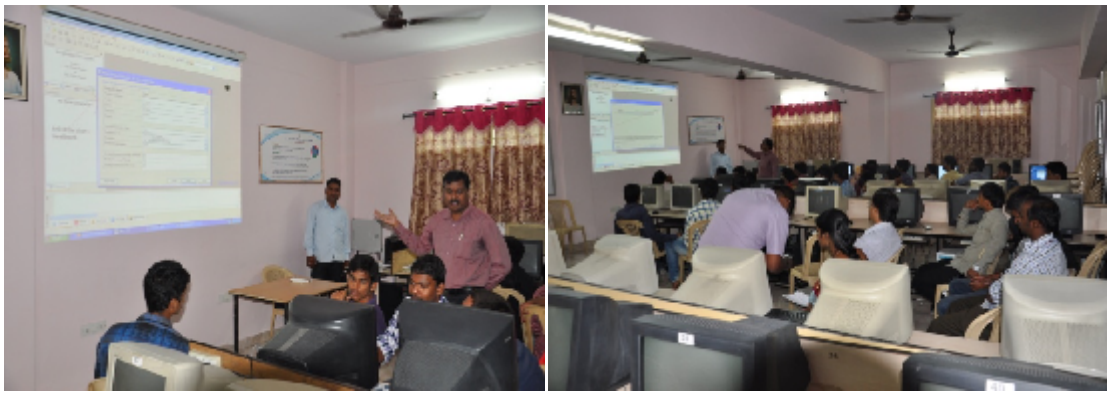
Objective:

To gain valuable hands-on experience. Incoming students with little or no VHDL knowledge be empowered with the ability to write efficient hardware designs and perform high-level HDL simulations.

About the Activity (few lines):

This workshop addresses targeting Xilinx devices specifically and FPGA devices in general. The information gained can be applied to any digital design by using a top-down synthesis design approach. This workshop combines insightful lecture with practical lab exercises to reinforce key concepts.

Photos:



Technical Session